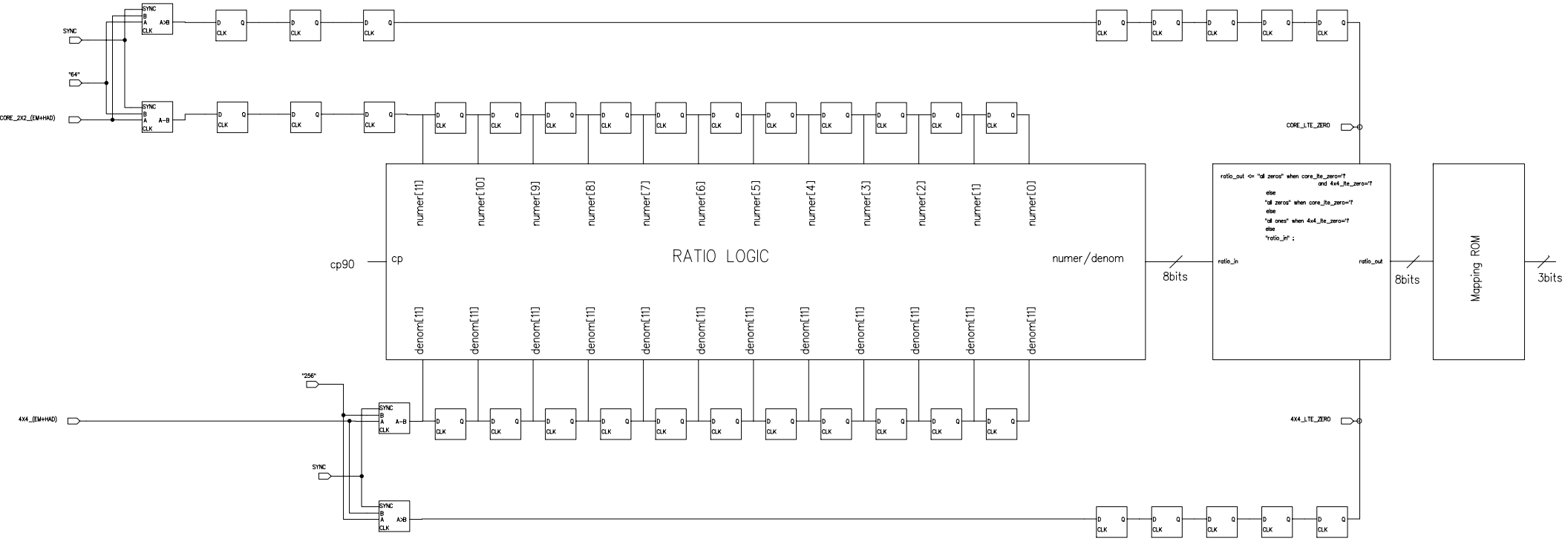
TAU LOGIC


SYNC
"64"
CORE_2X2_(EM+HAD)
SYNC
B
A
CLK
A>B
A-B
D
Q
CLK
numer[11]
numer[10]
numer[9]
numer[8]
numer[7]
numer[6]
numer[5]
numer[4]
numer[3]
numer[2]
numer[1]
numer[0]
cp90
cp
RATIO LOGIC
numer/denom
denom[11]
8bits
ratio_out <= "all zeros" when core_lte_zero='1'
and 4x4_lte_zero='1'
else
"all zeros" when core_lte_zero='1'
else
"all ones" when 4x4_lte_zero='1'
else
"ratio_in" ;
ratio_in
ratio_out
CORE_LTE_ZERO
Mapping ROM
3bits
"256"
4X4_(EM+HAD)
4X4_LTE_ZERO